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polysilicon layer over the drain region. The drain region is diffused into the substrate to form a source region. The polysilicon layers over the drain region and the source region serve as two plates of the capacitor. The dielectric layer separates the two plates. However, the polysilicon layer over the drain region may contact the polysilicon layer over the drain region, which results in the leakage of the capacitor. In order to prevent the polysilicon layer over the drain region from contacting the polysilicon layer over the drain region, a planarization process is performed to form the polysilicon layer over the drain region, and then an anisotropic etching process is performed to remove the polysilicon layer over the drain region to form a contact window that exposes a portion of the drain region. The polysilicon layer over the drain region is removed to form a metal silicide layer over the drain region. Thus, the contact window exposes a portion of the drain region and the metal silicide layer over the drain region

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